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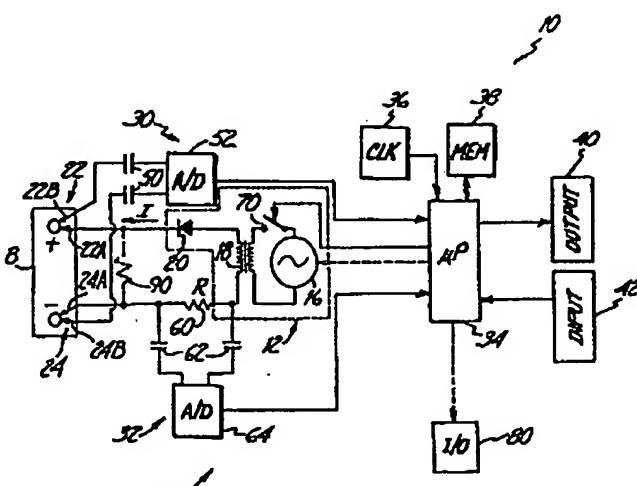
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INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification ⁶ : H02J 7/10	A1	(11) International Publication Number: WO 99/23738 (43) International Publication Date: 14 May 1999 (14.05.99)
<p>(21) International Application Number: PCT/US98/22671</p> <p>(22) International Filing Date: 26 October 1998 (26.10.98)</p> <p>(30) Priority Data: 08/962,754 3 November 1997 (03.11.97) US</p> <p>(71) Applicant: MIDTRONICS, INC. [US/US]; 8230 South Madison Street, Burr Ridge, IL 60521 (US).</p> <p>(72) Inventors: BERTNESS, Kevin, I.; 1317 McClurg Drive, Batavia, IL 60510 (US). McSHANE, Stephen, J.; 3521 Madison, Oak Brook, IL 60521 (US).</p> <p>(74) Agents: CHAMPLIN, Judson, K. et al.; Westman, Champlin & Kelly, P. A., International Centre, Suite 1600, 900 Second Avenue South, Minneapolis, MN 55402-3319 (US).</p>		(81) Designated States: AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CU, CZ, DE, DK, EE, ES, FI, GB, GE, GH, GM, HU, ID, IL, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, UA, UG, UZ, VN, YU, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).
<p>Published <i>With international search report.</i></p> <p>(54) Title: METHOD AND APPARATUS FOR CHARGING A BATTERY</p> 		
<p>(57) Abstract</p> <p>An apparatus (10) for charging a battery (8) includes a first electrical connector (22) adapted to connect to a positive terminal of the battery (8) and a second electrical connector (24) adapted to connect to a negative terminal of the battery (8). An electrical charging source (18) couples to the first and second electrical connectors (22, 24) to charge the battery (8). Voltage measurement circuitry (52) couples to the first and second electrical connectors (22, 24) and responsively provides a voltage output related to voltage across the battery (8). Current measurement circuitry (60, 64) also couples to the first and second electrical connectors (22, 24) and responsively provides a current output related to electrical current through the battery (8). State of charge measurement circuitry responsively provides a state of charge output as a function of the voltage output and the current output.</p>		

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METHOD AND APPARATUS FOR CHARGING A BATTERY

BACKGROUND OF THE INVENTION

The present invention relates to rechargeable storage batteries. More specifically, the present 5 invention relates to battery chargers of the type used to charge such batteries.

Chemical batteries which create electricity from chemical reactions have been known for many years. Such batteries are becoming increasing important and 10 have found uses throughout industry. These uses include automobiles, UPS systems, etc.

One advantage of chemical batteries, such as lead acid storage batteries, is that they can be charged and their chemical process reversed by forcing 15 electricity through the battery. Charging systems are widely known in the art and are widely available in the consumer market.

One of the most common techniques for recharging storage batteries is simply placing a voltage 20 source across the battery having a voltage which is greater than the battery voltage. The voltage difference will cause a charging current to flow through the battery causing a reversal of the chemical reaction. The charging current decreases as the voltage difference 25 between the charging voltage and the battery voltage decreases. Typically, the charging voltage is selected to be greater than the nominal battery voltage in order to cause a slight overcharge of the battery. The battery is deemed to be "charged" when the battery will 30 accept no additional current. Frequently, this is through a simple visual inspection of an amp meter on the battery charger by the user of the battery charger. The battery charger may then be switched off.

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However, such a simple technique for recharging a battery, although inexpensive, does not provide optimum battery charging and provides very little information about the battery itself. The device 5 does not permit optimal rapid charging of the battery and may lead to excessive overcharging of the battery that can permanently damage the battery and even lead to boiling of the battery electrochemicals causing an unsafe environment. On the other hand, undercharging of 10 a battery results in a battery that is not capable of providing its full potential output. These problems are exacerbated in situations where the battery is rapidly charged using large charging currents.

U.S. Patent No. 4,956,597, issued September 15, 1990 to Heavey et al. and is assigned to American Monarch Corporation, and entitled "METHOD AND APPARATUS FOR CHARGING BATTERIES" describes one technique in which battery voltage is monitored in an attempt to determine when a battery is fully charged. Another example is 20 shown in U.S. Patent No. 5,642,031, entitled BATTERY RECHARGING SYSTEM WITH STATE OF CHARGE DETECTION THAT INITIALLY DETECTS WHETHER A BATTERY TO BE CHARGED IS ALREADY AT OR NEAR FULL CHARGE TO PREVENT OVERCHARGING, issued June 24, 1997 to Brotto. The Brotto patent 25 describes a technique in which the charging current is periodically pulsed and the voltage decay is monitored.

SUMMARY OF THE INVENTION

An apparatus for charging a battery includes a first electrical connection adapted to connect to a 30 positive terminal of the battery and a second electrical connector adapted to connect to a negative terminal of the battery. An electrical charging source couples to the first and second electrical connectors to charge the battery. Voltage measurement circuitry couples to the

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first and second electrical connectors and responsively provides a voltage output related to voltage across the battery. Current measurement circuitry also couples to the first and second electrical connectors and 5 responsively provides a current output related to electrical current through the battery. State of charge measurement circuitry responsively provides a state of charge output as a function of the voltage output and the current output. One aspect of the invention 10 includes a state of charge output which increases as impedance decreases (and decreases as admittance increases) as determined by the voltage and current outputs. Another aspect of the invention includes battery rating storage circuitry which stores a rating 15 related to the battery in a fully charged condition and the state of charge output is a function of a measured state of charge and the battery rating. This may be expressed, for example, as a percentage charge.

BRIEF DESCRIPTION OF THE DRAWINGS

20 Figure 1 is a simplified block diagram of a battery charging system in accordance with the present invention.

Figure 2 is a graph of CCA versus time.

25 Figure 3 shows an output display in accordance with one embodiment of the present invention.

Figure 4 is a graph of CCA versus time for a battery with a bad cell.

Figure 5 is a graph of terminal voltage versus time for three batteries under a load test.

30 DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

The present invention provides an apparatus for charging a battery which is also capable of monitoring the condition of the battery. Such monitoring can be used to provide information to an

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operator, or to provide feedback to control the charging. The invention uses the charging current and voltage themselves to advantageously determine battery condition. Thus, a battery charger in accordance with 5 the present invention is capable of determining the status of the battery, making advanced decisions about charging the battery and selecting a particular charging profile used in such charging.

Figure 1 is a simplified block diagram of a 10 battery charging system 10 in accordance with the present invention coupled to storage battery 8. System 10 includes battery charger circuitry and test circuitry 14. Battery charge circuitry 12 generally includes AC source 16, transformer 18 and rectifier 20. System 10 15 couples to battery 8 through electrical connection 22 which couples to the positive terminal of battery 8 and electrical connection 24 which couples to the negative terminal of battery 8. In one preferred embodiment, a four point (or Kelvin) connection technique is used in 20 which battery charge circuitry 12 couples to battery 8 through electrical connections 22A and 24B while battery testing circuitry 14 couples to battery 8 through electrical connections 22A and 24B.

Battery testing circuitry 14 includes voltage 25 measurement circuitry 30 and current measurement circuitry 32 which provide outputs to microprocessor 34. Microprocessor 34 also couples to a system clock 36 and memory 38 which is used to store information and programming instructions. In the embodiment of the 30 invention shown in Figure 1, microprocessor 34 also couples to user output circuitry 40 and user input circuitry 42.

Voltage measurement circuitry 34 includes capacitors 50 which couple analog to digital converter

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52 to battery 8 through electrical connections 22B and 24B. Any type of coupling mechanism may be used for element 50 and capacitors are merely shown as one preferred embodiment. Further, the device may also 5 couple to DC signals. Current measurement circuitry 32 includes a shunt resistor (R) 60 and coupling capacitors 62. Shunt resistor 60 is coupled in series with battery charging circuitry 12. Other current measurement techniques are within the scope of the invention 10 including Hall-Effect sensors, magnetic or indicative coupling, etc. An analog to digital converter 64 is connected across shunt resistor 60 by capacitor 62 such that the voltage provided to analog to digital converter 64 is proportional to a current I flowing through 15 battery 8 due to charging circuitry 12. Analog to digital converter 64 provides a digitized output representative of this current to microprocessor 34.

During operation, AC source 16 is coupled to battery 8 through transformer 18 and rectifier 20. 20 Rectifier 20 provides half way rectification such that current I has a non-zero DC value. Of course, full wave rectification or other AC sources may also be used. Analog to digital converter 64 provides a digitized output to microprocessor 34 which is representative of 25 current I flowing through battery 8. Similarly, analog to digital converter 52 provides a digitized output representative of the voltage across the positive and negative terminals of battery 8. Analog to digital converters 52 and 64 are capacitively coupled to battery 30 8 such that they measure the AC components of the charging signal.

Microprocessor 34 determines the conductance of battery 8 based upon the digitized current and voltage information provided by analog to digital

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converters 64 and 52, respectively. Microprocessor 34 counts the conductance of battery 8 as follows:

$$\text{Conductance-} G = \frac{I}{V} \quad \text{Eq. 1}$$

5 where I is the charging current and V is the charging voltage across battery 8. Note that in one preferred embodiment the Kelvin connections allow more accurate voltage determination because these connections do not carry substantial current to cause a resultant drop in the voltage measured.

10 In accordance with the present invention, the battery conductance is used to monitor charging of battery 8. Specifically, it has been discovered that as a battery is charged the conductance of the battery rises. This rise in conductance can be monitored in 15 microprocessor 34 to determine when the battery has been fully charged. For example, if the rate of the rise in conductance slowly decreases, such that the conductance reaches a substantially constant value, microprocessor 34 determines that battery 8 is fully charged and 20 disconnect charging circuitry 12 using switch 70. Further, in one aspect of the present invention, microprocessor 34 responsively controls the rate of charge by adjusting AC source 16 to reduce the likelihood that battery 8 is damaged by significant 25 overcharge.

Furthermore, microprocessor 34 can calculate cold cranking amps (CCA) of battery 8 using the formula:

$$\text{CCA} = K \cdot G \quad \text{Eq. 2}$$

30 where K is constant which may be selected for a specific battery and G is given in Equation 1. Figure 2 is a graph of CCA versus time in minutes for three different

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batteries having three different nominal CCA ratings. This particular graph shows that after about 25 to 30 minutes, each battery has reached its nominal CCA rating.

5 One aspect of the invention includes storing information in microprocessor 34 or memory 38 which relates to battery 8. For example, this information could be the battery's nominal CCA rating as input through input 42 by an operator. Further, the make and
10 model of the battery may be input by an operator through input 42 and information related to that specific battery type recovered from memory 38. In general, the rating of the battery may be input in the form of CCA, amp hours, RC, JIS number, stock number, battery
15 construction or chemistry, etc. For example, if a nominal or reference conductance ($G_{REFERENCE}$) is stored in memory, a relative conductance determination can be made by microprocessor 34 using the equation:

$$\text{Relative Conductance (\%)} = \frac{G_{measured}}{G_{reference}} \times 100, \quad \text{Eq. 3}$$

20 where $G_{measured}$ is the battery conductance in accordance with Equation 1. Generally, this reference conductance is determined based upon type and characteristics of battery 8. This technique is described in U.S. Patent No. 5,140,269, entitled ELECTRONIC TESTER FOR ASSESSING
25 BATTERY/CELL CAPACITY, issued August 18, 1992 to Champlin. This may be converted into a display for output on output 40 such that an operator may monitor the charging of battery 8. For example, Figure 3 shows one embodiment of output 40 in which a bar graph is provided with indications for "empty" and "full." In
30 the embodiment shown in Figure 3, a half reading "full"

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is shown. This may be implemented through an LED display, for example. Other examples of desirable outputs include outputs which appear as a gauge or other visual indication of the battery condition. Other types 5 of outputs include outputs indicating the recovering of amp hours, state of charge, reserve capacity, time to full charge or run time remaining. This may be shown in percentages, numerically, graphically, etc.

One general aspect of the present invention 10 includes a technique for providing feedback control of a battery charger where the feedback control is a function of charging voltage and charging current. In one embodiment, bad cells or shorted cells are detected when the rate of change of the conductance is greater 15 than a predetermined threshold. For example, Figure 4 is a graph of CCA versus time for a battery (nominal conductance of 500 CCA) being charged in accordance with the invention which developed a bad cell during testing. The graph of Figure 4 shows an abnormal change in 20 conductance in comparison to the graphs of Figure 2. This can be used to control the charging rate, or to stop the charging rate and give a fault indication using output 40. Further, an initial load test may be applied to the battery using load resistance 90 to determine if 25 there is a bad cell by observing a resultant decline in voltage versus time using voltage measurement circuitry 30. This is illustrated in the graph of Figure 5 which shows battery terminal voltage versus time for two good batteries and one battery with a bad cell. Further, 30 other diagnostics may be performed such as those described in U.S. Patent No. 5,574,355, entitled METHOD AND APPARATUS FOR DETECTION AND CONTROL OF THERMAL RUNAWAY IN A BATTERY UNDER CHARGE, issued November 12, 1996 to McShane et al.

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The present invention advantageously uses the charging current and voltage to determine the condition of the battery. This allows for continuous testing during battery charging and the battery charger does not 5 need to be taken off line. Further, the measurement is not adversely affected by electrical noise in the system.

Another aspect of the present invention includes an I/O port or link 80 which is adapted to be 10 coupled to an external device, such as an external battery tester, printer, storage device, automotive analysis device, etc. This may be a physical link, or, in one embodiment, this I/O link is through an infrared (IR), radio frequency (RF), or ultrasonic connection 15 such that a physical link between the two units is not necessary. For example, the results from a previously performed battery test can be supplied to system 10 by a separate battery tester, whereby the initial condition 20 of battery 8 is used during charging. Similarly, an external or separate device can communicate information such as the model number or type of battery being charged.

The present invention advantageously uses a Kelvin connection in which the electrical connection to 25 the battery includes two connections for charging and two connections for a separate voltage measurement.

Additionally, during charging, the rate of increase of the voltage may be selectively monitored to determine charging efficiency and other characteristics 30 of the battery.

One type of output that can be provided on output 40 includes various types of auditing codes as those set forth in co-pending application Serial No. 08/681,730, entitled "METHOD AND APPARATUS FOR AUDITING

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A BATTERY TEST", filed July 29, 1996 which is commonly assigned with the present application. Additional codes include the sequence code which can indicate the order in which tests were made on an individual battery tester 5 or a time code which indicates the date and time at which a test was made.

In general, although the formulas and techniques set forth herein discuss determination of conductance, the present invention may also be implied 10 to impedance measurements, resistance measurements or admittance measurements of the storage battery. Further, although a microprocessor and digital circuitry is disclosed herein, the present invention may also be implemented in analog circuitry. In general, various 15 techniques for measuring battery conductance are shown in a number of patents including, for example, U.S. Patent No. 3,873,911, issued March 25, 1975, to Champlin, entitled ELECTRONIC BATTERY TESTING DEVICE; U.S. Patent No. 3,909,708, issued September 30, 1975, to Champlin, 20 entitled ELECTRONIC BATTERY TESTING DEVICE; U.S. Patent No. 4,816,768, issued March 28, 1989, to Champlin, entitled ELECTRONIC BATTERY TESTING DEVICE; U.S. Patent No. 4,825,170, issued April 25, 1989, to Champlin, entitled ELECTRONIC BATTERY TESTING DEVICE WITH 25 AUTOMATIC VOLTAGE SCALING; U.S. Patent No. 4,881,038, issued November 14, 1989, to Champlin, entitled ELECTRONIC BATTERY TESTING DEVICE WITH AUTOMATIC VOLTAGE SCALING TO DETERMINE DYNAMIC CONDUCTANCE; U.S. Patent No. 4,912,416, issued March 27, 1990, to Champlin, 30 entitled ELECTRONIC BATTERY TESTING DEVICE WITH STATE-OF-CHARGE COMPENSATION; U.S. Patent No. 5,140,269, issued August 18, 1992, to Champlin, entitled ELECTRONIC TESTER FOR ASSESSING BATTERY/CELL CAPACITY; U.S. Patent 4,322,685, issued March 30, 1982, entitled AUTOMATIC

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BATTERY ANALYZER INCLUDING APPARATUS FOR DETERMINING PRESENCE OF SINGLE BAD CELL; U.S. Patent No. 5,343,380, issued August 30, 1994, entitled "METHOD AND APPARATUS FOR SUPPRESSING TIME VARYING SIGNALS IN BATTERIES 5 UNDERGOING CHARGING OR DISCHARGING"; U.S. Patent No. 5,572,136, issued November 5, 1996, entitled "ELECTRONIC BATTERY TESTER WITH AUTOMATIC COMPENSATION FOR LOW STATE-OF-CHARGE"; U.S. Patent No. 5,574,355, issued November 12, 1996, entitled METHOD AND APPARATUS FOR 10 DETECTION AND CONTROL OF THERMAL RUNAWAY IN A BATTERY UNDER CHARGE"; U.S. Patent No. 5,585,728, issued December 17, 1996, entitled "ELECTRONIC BATTERY TESTER WITH AUTOMATIC COMPENSATION FOR LOW STATE-OF-CHARGE"; U.S. Patent No. 5,598,098, issued January 28, 1997, 15 entitled "ELECTRONIC BATTERY TESTER WITH VERY HIGH NOISE IMMUNITY"; and U.S. Patent No. 5,592,093, issued January 7, 1997, entitled "ELECTRONIC BATTERY TESTING DEVICE LOOSE TERMINAL CONNECTION DETECTION VIA A COMPARISON CIRCUIT".

20 Although the present invention has been described with reference to preferred embodiments, workers skilled in the art will recognize that changes may be made in form and detail without departing from the spirit and scope of the invention. The various 25 voltages and currents measured herein are set forth as alternating signals and their measurements may be through RMS values, peak-to-peak measurements, etc. However, other techniques may be employed and DC signals may also be monitored. In a typical battery charger, 30 the AC component of the charging signal is related to the line frequency and thus, in the United States, is typically 60 Hz or 120 Hz. However, other frequencies may also be employed. Further, the charge signal may be a stepped DC signal and the voltage and current

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measurement circuitry responsive to step DC signals. In general, the invention determines battery and/or charging conditions based upon a ratio of charging voltage and charging current.

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WHAT IS CLAIMED IS:

1. An apparatus for charging a battery, comprising:
 - a first electrical connector adapted to connect to a positive terminal of the battery;
 - a second electrical connector adapted to connect to a negative terminal of the battery;
 - an electrical charging source providing a charge signal to the first and second connectors to charge the battery;
 - voltage measurement circuitry coupled to the first and second electrical connectors responsively providing a sensed voltage output related to a charging voltage;
 - current measurement circuitry coupled to the first and second electrical connectors responsively providing a sensed current output related to a charging current;
 - and
 - state of charge measurement circuitry responsively providing state of charge output as a function of the sensed voltage output and the sensed current output.
2. The apparatus of claim 1 wherein the state of charge output increases as battery impedance decreases and increases as battery admittance increases based upon the sensed voltage and current outputs.
3. The apparatus of claim 1 including battery rating storage circuitry storing a rating related to the battery in a fully charged condition and wherein the

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state of charge output is a function of measured state of charge and the stored battery rating.

4. The apparatus of claim 3 wherein the state of charge output is a percentage.

5. The apparatus of claim 1 wherein the electrical charging source provides a rectified signal to charge the battery.

6. The apparatus of claim 1 wherein the current measurement circuitry includes a resistor placed in series with the electrical charging source to generate a voltage drop thereacross.

7. The apparatus of claim 1 wherein the current measurement circuitry includes a Hall-Effect sensor which provides the sensed current output as a voltage.

8. The apparatus of claim 1 wherein the current measurement circuitry includes a magnetic pickup device which provides the sensed current output as a voltage.

9. The apparatus of claim 1 wherein the state of charge output is a function of the sensed current output divided by the sensed voltage output.

10. The apparatus of claim 1 wherein the state of charge measurement circuitry comprises a microprocessor.

11. The apparatus of claim 1 wherein the charge signal from the electrical charging source is responsive to the state of charge output from the state of charge measurement circuitry.

12. The apparatus of claim 11 wherein the charge signal is reduced as the state of charge of the battery increases.

13. The apparatus of claim 3 including input circuitry for receiving an input related to battery rating for storage in the battery rating storage circuitry.

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14. The apparatus of claim 13 wherein the input is a user input.
15. The apparatus of claim 14 wherein the user input is selected from the group consisting of battery make, battery model, battery type, battery part number and battery rating.
16. The apparatus of claim 1 including a load resistance for performing a load test on the battery.
17. The apparatus of claim 1 wherein the charge signal includes an AC component.
18. The apparatus of claim 17 wherein the voltage and current measurement circuits are responsive to AC signals.
19. The apparatus of claim 18 wherein the voltage and current measurement circuits measure RMS.
20. The apparatus of claim 18 wherein the voltage and current measurement circuits measure peak-to-peak.
21. The apparatus of claim 1 wherein the charge signal includes a stepped DC component.
22. The apparatus of claim 21 wherein the voltage and current circuits are responsive to step changes in DC levels.
23. The apparatus of claim 1 wherein the state of charge measurement circuitry provides an output indicative of a bad cell in the battery in response to the sensed voltage and current outputs.
24. The apparatus of claim 1 including a parameter input and the state of charge output is further a function of a parameter input.
25. The apparatus of claim 24 wherein the input is received over a non-physical link.
26. The apparatus of claim 25 wherein the non-physical link is selected from the group consisting of IR, RF, and ultrasonic.

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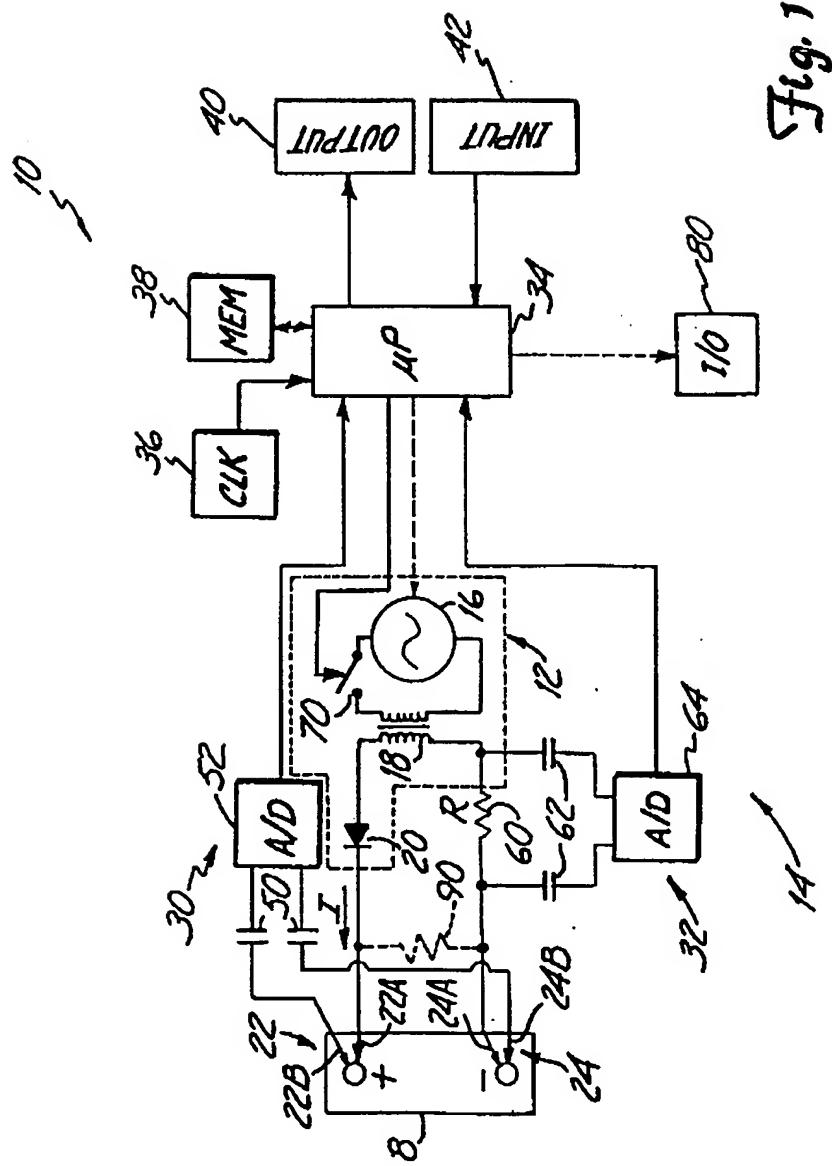


Fig. 1

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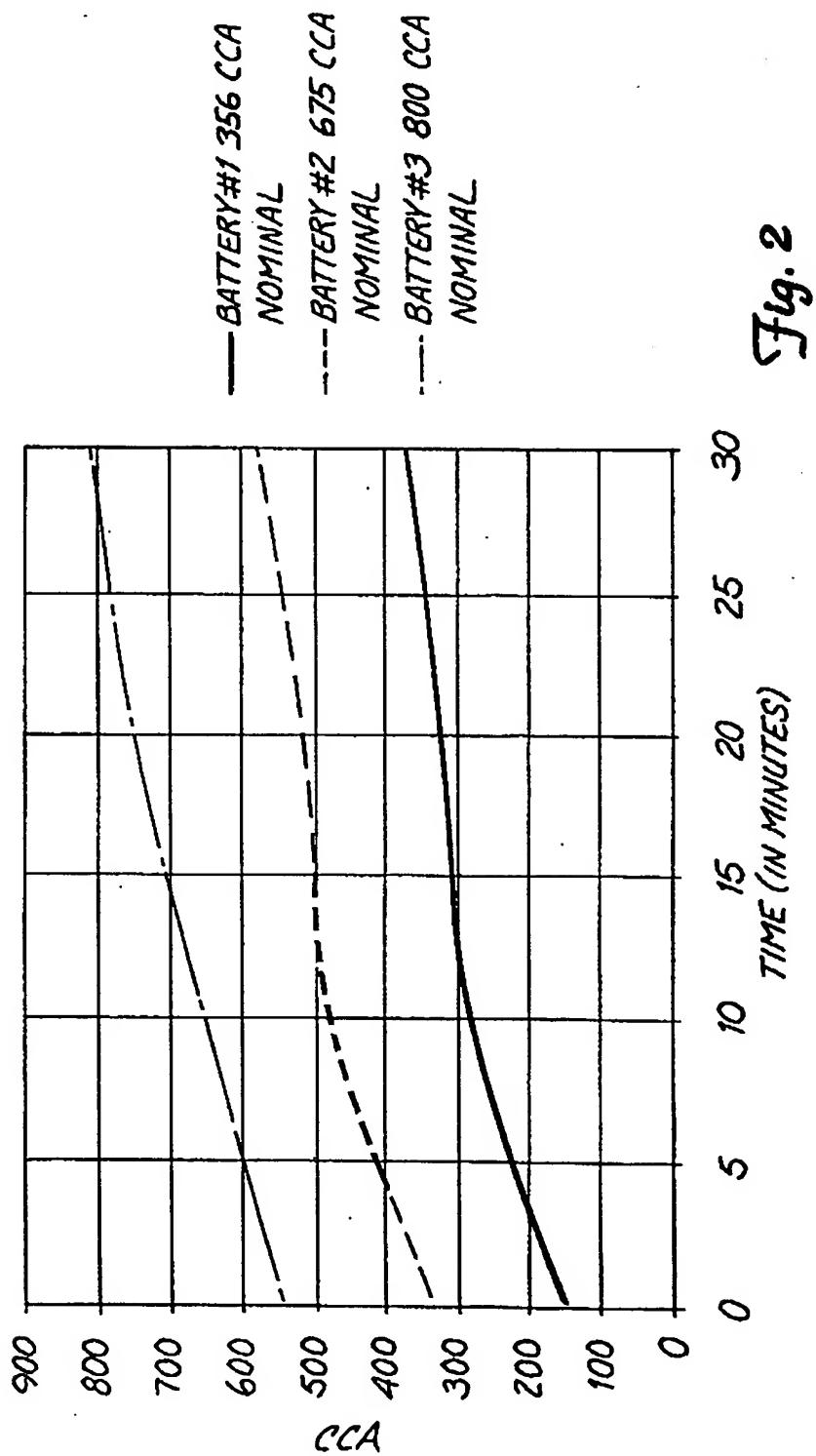


Fig. 2

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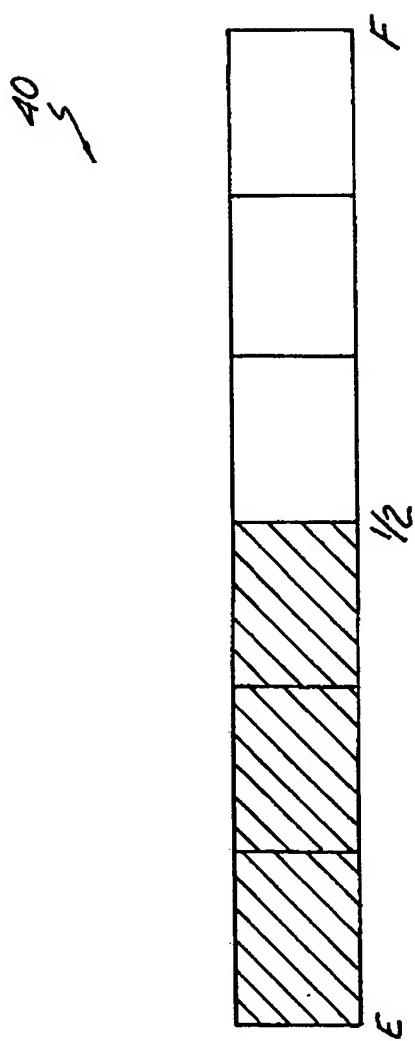


Fig. 3

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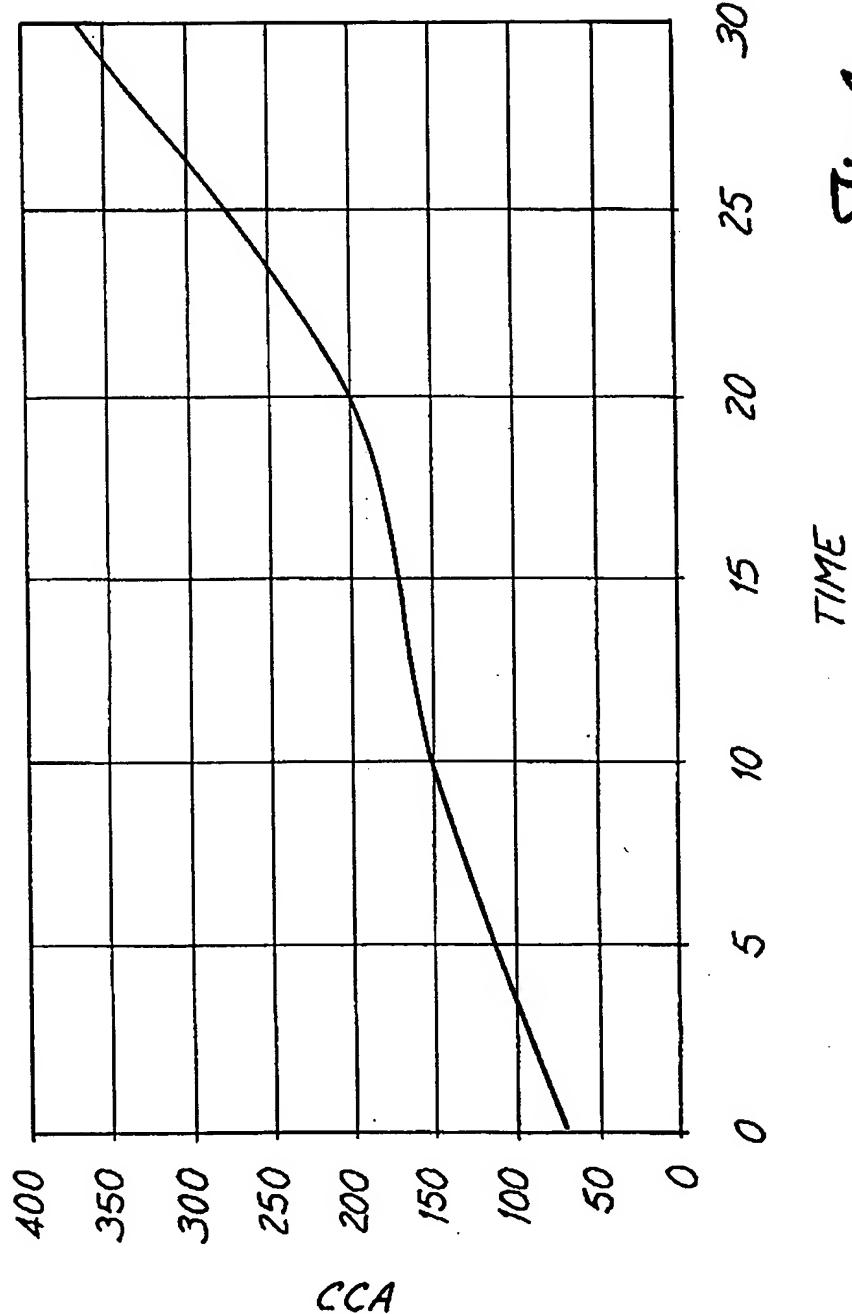
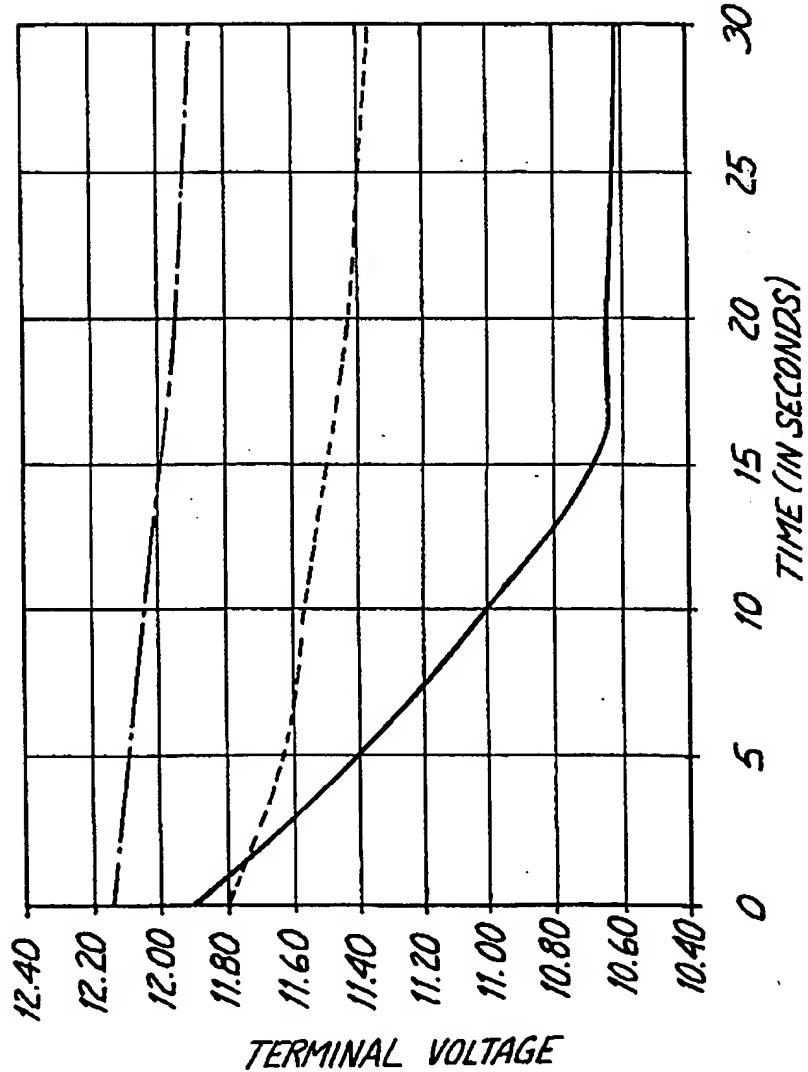


Fig. 4

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— BATTERY #1 GOOD
- - - BATTERY #2 GOOD
— BATTERY #3 BAD CELL

Fig. 5



INTERNATIONAL SEARCH REPORT

International Application No
PCT/US 98/22671

A. CLASSIFICATION OF SUBJECT MATTER
IPC 6 H02J7/10

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 6 H02J G01R

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the International search (name of data base and, where practical, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	US 4 423 379 A (JACOBS WILLIAM R ET AL) 27 December 1983 see the whole document	3,10, 13-15,24
A	US 5 457 377 A (JONSSON LENNART ET AL) 10 October 1995 see the whole document	1-26
Y	US 3 984 762 A (DOWGIALLO JR EDWARD J) 5 October 1976 see the whole document	16,18
X	GB 2 088 159 A (HARMER & SIMMONS LTD) 3 June 1982 see the whole document	1,6,9, 11,12
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Date of the actual completion of the International search

22 January 1999

Date of mailing of the International search report

09/02/1999

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INTERNATIONAL SEARCH REPORT

International Application No
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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 5 343 380 A (CHAMPLIN KEITH S) 30 August 1994 cited in the application see the whole document	1-26
A	PATENT ABSTRACTS OF JAPAN vol. 010, no. 089 (P-444), 8 April 1986 & JP 60 225078 A (NISSAN JIDOSHA KK), 9 November 1985 see abstract	1-24
A	PATENT ABSTRACTS OF JAPAN vol. 017, no. 253 (E-1367), 19 May 1993 & JP 04 372536 A (OMRON CORP), 25 December 1992 see abstract	23
Y	PATENT ABSTRACTS OF JAPAN vol. 016, no. 398 (P-1408), 24 August 1992 & JP 04 131779 A (FUJI ELECTRIC CO LTD), 6 May 1992 see abstract	23
Y	PATENT ABSTRACTS OF JAPAN vol. 016, no. 105 (P-1325), 16 March 1992 & JP 03 282276 A (YUASA BATTERY CO LTD), 12 December 1991 see abstract	17, 18
A	PATENT ABSTRACTS OF JAPAN vol. 016, no. 096 (P-1322), 10 March 1992 & JP 03 274479 A (YUASA BATTERY CO LTD), 5 December 1991 see abstract	9

INTERNATIONAL SEARCH REPORT

Information on patent family members

National Application No

PCT/US 98/22671

Patent document cited in search report	Publication date	Patent family member(s)		Publication date
US 4423379 A	27-12-1983	NONE		
US 5457377 A	10-10-1995	FI	924425 A	02-04-1994
		DE	4333520 A	07-04-1994
		GB	2271229 A,B	06-04-1994
US 3984762 A	05-10-1976	NONE		
GB 2088159 A	03-06-1982	NONE		
US 5343380 A	30-08-1994	AU	5606594 A	08-06-1994
		WO	9411938 A	26-05-1994